

HFA32PA120C

HEXFRED™

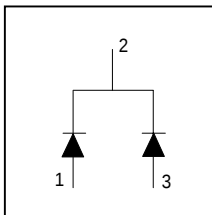
Ultrafast, Soft Recovery Diode

Features

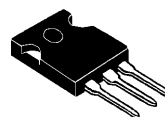
- Ultrafast Recovery
- Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- Specified at Operating Conditions

Benefits

- Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- Higher Frequency Operation
- Reduced Snubbing
- Reduced Parts Count



per Leg
 $V_R = 1200V$
 $V_F(\text{typ.}) = 2.3V$
 $I_{F(AV)} = 16A$
 $Q_{rr}(\text{typ.}) = 260nC$
 $I_{RRM}(\text{typ.}) = 5.8A$
 $t_{rr}(\text{typ.}) = 30ns$



TO-247AC

Description

International Rectifier's HFA32PA120C is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 volts and 16 amps continuous current, the HFA32PA120C is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA32PA120C is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

Absolute Maximum Ratings (per Leg)

	Parameter	Max	Units
V_R	Cathode-to-Anode Voltage	1200	V
$I_F @ T_C = 100^\circ C$	Continuous Forward Current	16	A
I_{FSM}	Single Pulse Forward Current	190	
I_{FRM}	Maximum Repetitive Forward Current	64	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	151	$^\circ C$
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	60	
T_J	Operating Junction and	-55 to +150	W
T_{STG}	Storage Temperature Range		

* $125^\circ C$

Electrical Characteristics (per Leg) @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
V_{BR}	Cathode Anode Breakdown Voltage	1200			V	$I_R = 100\mu\text{A}$
V_{FM}	Max Forward Voltage		2.5	3.0	V	$I_F = 16\text{A}$
			3.2	3.93		$I_F = 32\text{A}$ See Fig. 1
			2.3	2.7		$I_F = 16\text{A}$, $T_J = 125^\circ\text{C}$
I_{RM}	Max Reverse Leakage Current		0.75	20	μA	$V_R = V_R$ Rated See Fig. 2
			375	2000		$T_J = 125^\circ\text{C}$, $V_R = 0.8 \times V_R$ Rated
C_T	Junction Capacitance		27	40	pF	$V_R = 200\text{V}$ See Fig. 3
L_S	Series Inductance		8.0		nH	Measured lead to lead 5mm from package body

Dynamic Recovery Characteristics (per Leg) @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
t_{rr}	Reverse Recovery Time		30		ns	$I_F = 1.0\text{A}$, $di/dt = 200\text{A}/\mu\text{s}$, $V_R = 30\text{V}$
t_{rr1}	See Fig. 5, 10		90	135		$T_J = 25^\circ\text{C}$
t_{rr2}			164	245		$T_J = 125^\circ\text{C}$
I_{RRM1}	Peak Recovery Current See Fig. 6		5.8	10	A	$T_J = 25^\circ\text{C}$
I_{RRM2}			8.3	15		$T_J = 125^\circ\text{C}$
Q_{rr1}	Reverse Recovery Charge		260	675	nC	$T_J = 25^\circ\text{C}$
Q_{rr2}	See Fig. 7		680	1838		$T_J = 125^\circ\text{C}$
$di_{(rec)M}/dt1$	Peak Rate of Fall of Recovery Current During t_b See Fig. 8		120			$T_J = 25^\circ\text{C}$
$di_{(rec)M}/dt2$			76		$\text{A}/\mu\text{s}$	$T_J = 125^\circ\text{C}$

Thermal - Mechanical Characteristics

	Parameter	Min	Typ	Max	Units
$T_{lead}^{①}$	Lead Temperature			300	$^\circ\text{C}$
R_{thJC}	Thermal Resistance, Junction to Case			0.83	K/W
$R_{thJA}^{②}$	Thermal Resistance, Junction to Ambient			80	
$R_{thCS}^{③}$	Thermal Resistance, Case to Heat Sink		0.50		
Wt	Weight		2.0		g
			0.07		(oz)
	Mounting Torque	6.0		12	Kg-cm
		5.0		10	lbf-in

① 0.063 in. from Case (1.6mm) for 10 sec

② Typical Socket Mount

③ Mounting Surface, Flat, Smooth and Greased

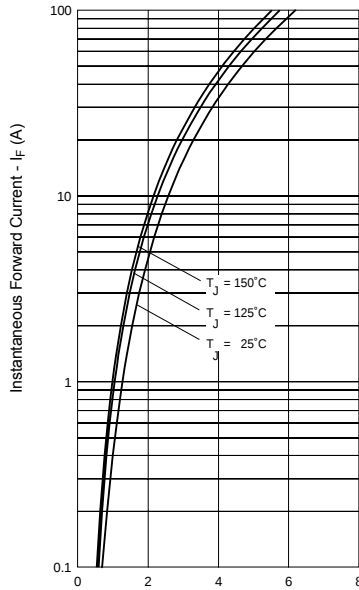


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

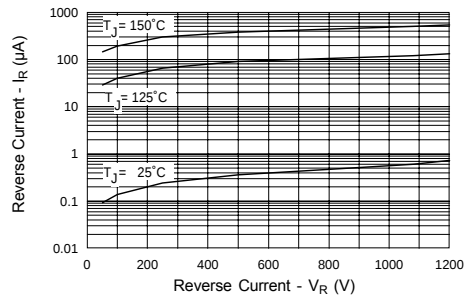


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

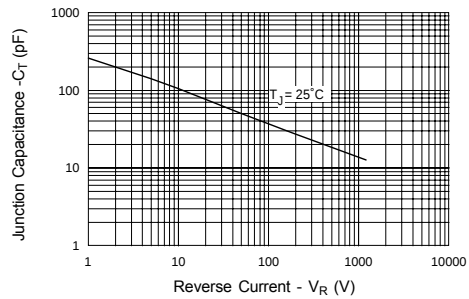


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

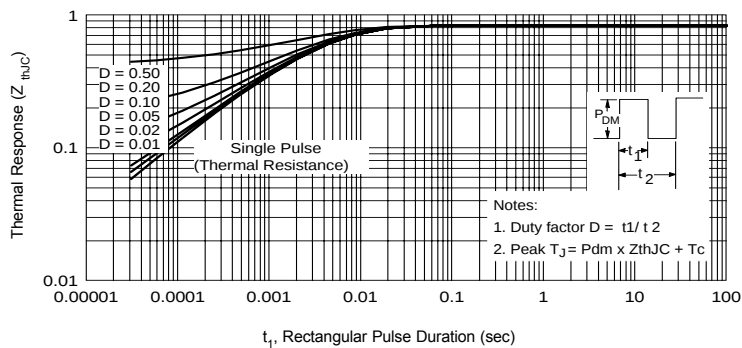


Fig. 4 - Maximum Thermal Impedance Z_{thjc} Characteristics

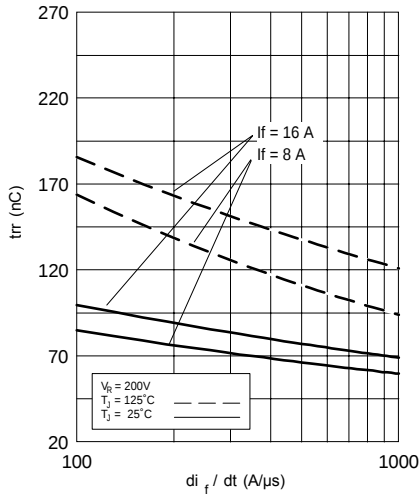


Fig. 5 - Typical Reverse Recovery vs. di_f/dt , (per Leg)

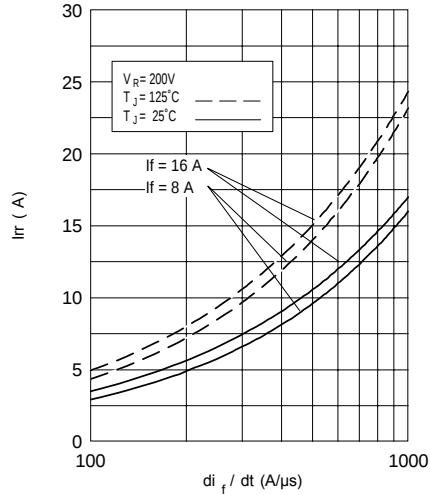


Fig. 6 - Typical Recovery Current vs. di_f/dt , (per Leg)

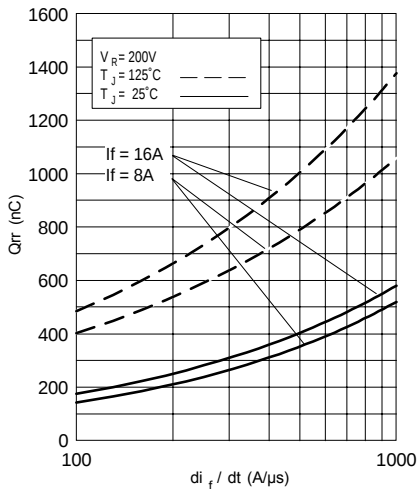


Fig. 7 - Typical Stored Charge vs. di_f/dt , (per Leg)

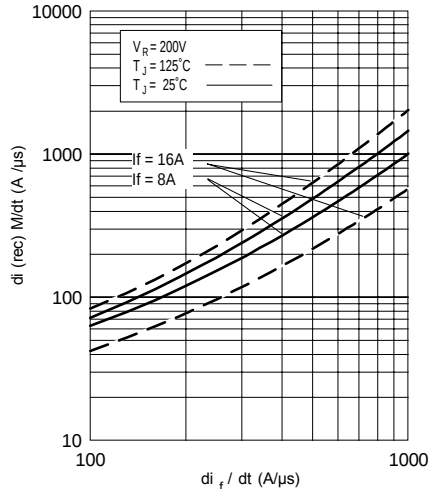


Fig. 8 - Typical $di_{(rec)}M/dt$ vs. di_f/dt , (per Leg)

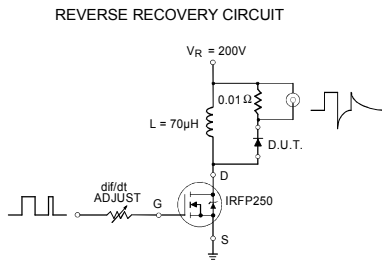


Fig. 9 - Reverse Recovery Parameter Test Circuit

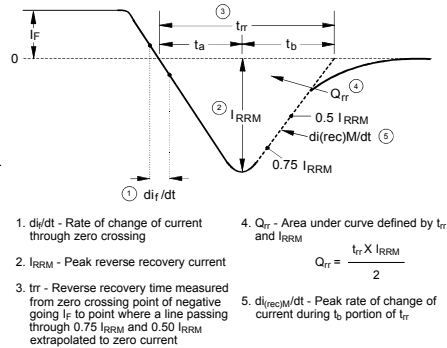


Fig. 10 - Reverse Recovery Waveform and Definitions

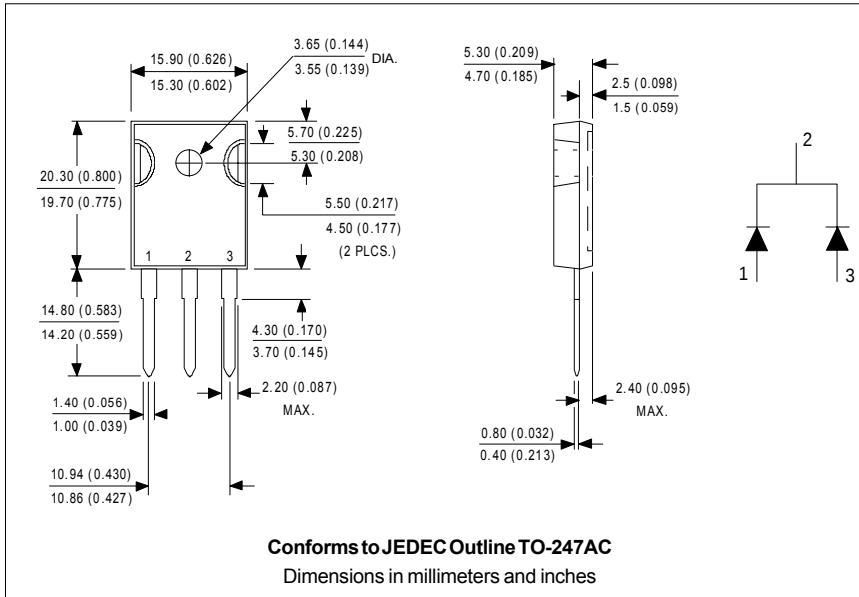
Ordering Information Table

Device Code

The diagram illustrates the Device Code structure with six positions, each represented by a black box with white text, connected by a horizontal line. Below each box is a circle containing a number from 1 to 6, indicating the position number.

Position	Code
1	HF
2	A
3	32
4	PA
5	120
6	C

1	-	Hexfred Family
2	-	Process Designator
	A	= subs. elec. irradi.
	B	= subs. Platinum
3	-	Current Rating (32 = 32A)
4	-	Package Outline (PA = TO-247, 3 pins)
5	-	Voltage Rating (120 = 1200V)
6	-	Configuration (C = Center Tap Common Cathode)

Outline Table

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

International
IOR Rectifier

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Visit us at www.irf.com for sales contact information. 05/01