

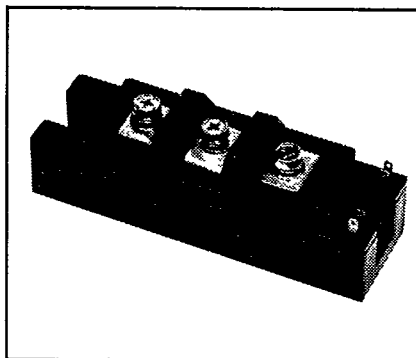
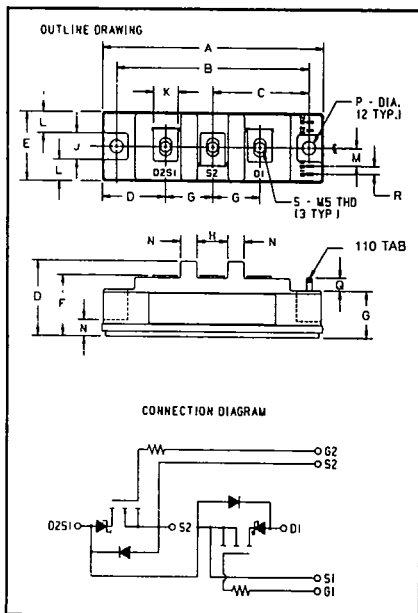


JD224505
JD225005

T-39-27

Powerex, Inc., Hillis Street, Youngwood, Pennsylvania 15697 (412) 925-7272
Powerex Europe, S.A., 428 Avenue G. Durand, BP107, 72003 Le Mans, France (43) 41.14.14

Dual FETMOD™
Power Modules
50 Amperes/450-500 Volts



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50 Amperes/450/500 Volts

450-500 Volts JD224505, JD225005
Outline Drawing

Dimension	Inches	Millimeters
A	4.252 Max	108 Max.
B	3.661 ± 0.12	93 ± 0.3
C	1.831	46.5
D	1.457	37
E	1.339	34
F	1.181 Max	30 Max.
G	906	23
H	.591	15
J	512	13
K	472	12
L	413	10.5
M	344	8.75
N	315	8
P	256 Dia	6.5 Dia.
Q	256 Min	6.5 Min
R	157	4
S	M5 Metric	M5

Description

Powerex Dual FETMOD™ Power Modules are designed for use in applications requiring high frequency switching and low loss control. The modules are isolated, consisting of two MOSFET Transistors with each MOSFET having a series Schottky blocking diode and a reverse parallel connected high speed diode.

Features:

- Isolated Mounting
- Vertical DMOS Chips
- Discrete Fast Recovery Feed-Back Diode and Series Schottky Diodes
- Low $R_{DS(on)}$
- Low Drive Requirement
- Internal Series Gate Resistors
- Fast Switching

Applications:

- AC Motor Control
- UPS Inverters
- Switch Mode Power Supply
- PWM Regulators

Ordering Information

Example: Select the complete eight digit module part number you desire from the table - i.e. JD225005 is a 500 Volt, 50 Ampere Dual FETMOD™ Module.

Type	V _{oss} Volts (×10)	Current Rating Amperes (×10)
JD22	45	05
JD22	50	05



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Maximum Ratings $T_J = 25^\circ\text{C}$ unless otherwise specified

	Symbol	JD224505/JD225005	Units
Junction Temperature	T_J	-40 to 150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-40 to 125	$^\circ\text{C}$
Drain Source Voltage	V_{DSS}	450/500	Volts
Gate-Source Voltage	V_{GSS}	± 20	Volts
Continuous Drain Current	I_D	30	Amperes
Continuous Diode Forward Current	I_{FM}	50	Amperes
Pulsed Drain Current Repetitive	I_{DM}	150	Amperes
Power Dissipation	P_T	310	Watts
Max. Mounting Torque Terminal Screws (M5)	—	17	in.-lb.
Max. Mounting Torque Mounting Screws (M6)	—	26	in.-lb.
Module Weight	—	250	Grams
V isolation	V_{RMS}	2500	Volts

Static Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	JD224505/JD225005			Units
			Min.	Typ.	Max.	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = V_{\text{DSS}}, V_{\text{GS}} = 0\text{V}$	—	—	1	mA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 0.8 V_{\text{DSS}}, V_{\text{GS}} = 0\text{V}$ $T_J = 125^\circ\text{C}$	—	—	10	mA
Gate Source Threshold	$V_{\text{GS(th)}}$	$I_D = 1\text{ mA}, V_{\text{DS}} = 10\text{V}$	2	3	4	Volts
Gate Source Leakage	$\pm I_{\text{GSS}}$	$\pm V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$	—	—	0.5	μA
Drain Source On State Resistance*	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 15\text{V}, I_D = 50\text{A}$ $V_{\text{GS}} = 15\text{V}, I_D = 50\text{A}, T_J = 150^\circ\text{C}$	—	.16	0.2	Ω
Drain Source On State Voltage*	$V_{\text{DS(on)}}$	$V_{\text{GS}} = 15\text{V}, I_D = 50\text{A}$ $V_{\text{GS}} = 15\text{V}, I_D = 50\text{A}, T_J = 150^\circ\text{C}$	—	8	10	Volts
Thermal Resistance, Case to Sink Lubricated	$R_{\theta\text{CS}}$	—	—	—	1.3	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta\text{JC}}$	Transistor Part	—	—	0.41	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta\text{JC}}$	Diode Part	—	—	1.3	$^\circ\text{C/W}$

* Pulse Test: Pulse width $\leq 10\mu\text{s}$



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JD225005

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Free Wheel Diode Characteristics $T_J = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	JD224505/JD225005			Units
			Min.	Typ.	Max.	
Diode Forward Voltage	V_{FM}	$I_{FM} = 50\text{A}, V_{GS} = 0\text{V}$	—	—	2.5	Volts
Reverse Recovery Time	t_{rr}	$I_{FM} = 50\text{A}, di_F/dt = 100\text{A}/\mu\text{s}, V_{GS} = 0\text{V}$	—	120	200	ns
Reverse Recovery Charge	Q_{rr}		—	650	—	μC

Dynamic Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	JD224505/JD225005			Units
			Min.	Typ.	Max.	
Forward Transconductance	g_{fs}	$I_D = 25\text{A}, V_{DS} = 10\text{V}$ $t_w \leq 300\mu\text{s}, \text{Duty} = 2\%$	10	—	—	mhos
Input Capacitance	C_{iss}		—	5500	9000	pf
Output Capacitance	C_{oss}	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{ Mhz}$	—	—	2000	pf
Reverse Transfer Capacitance	C_{rss}		—	—	700	pf
Total Gate Charge	Q_G	$V_{DD} = 0.8 V_{DSS}$ $V_{GS} = 10\text{V}, I_D = 50\text{A}$	—	600	—	nC
Turn On Time**	t_{on}	$V_{DD} = 0.5 V_{DSS}$	—	—	500	ns
Turn Off Time**	t_{off}	$I_D = 25\text{A}, V_{GS} = 15\text{V}$ $R_{GEN} = R_{GS} = 50\Omega$	—	—	1300	ns

** Turn on Time (t_{on}) = Turn on Delay ($t_{d(on)}$) + Rise Time (t_r)

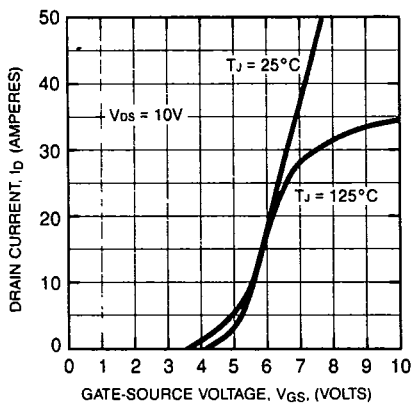
Turn-off Time (t_{off}) = Turn off Delay ($t_{d(off)}$) + Fall Time (t_f)



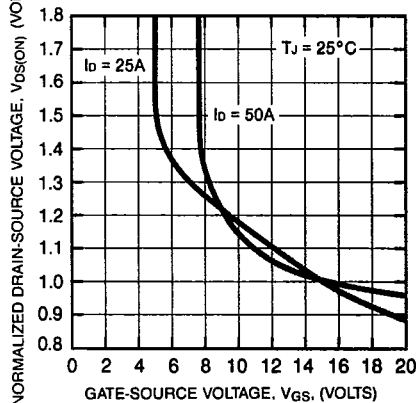
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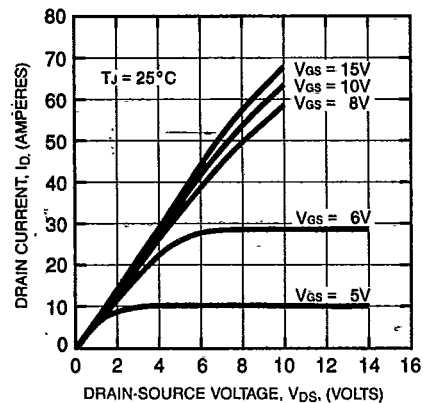
TRANSFER CHARACTERISTICS (TYPICAL)



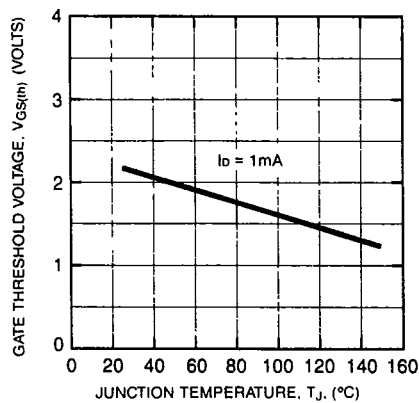
NORAMALIZED DRAIN-SOURCE ON-STATE VOLTAGE (TYPICAL)



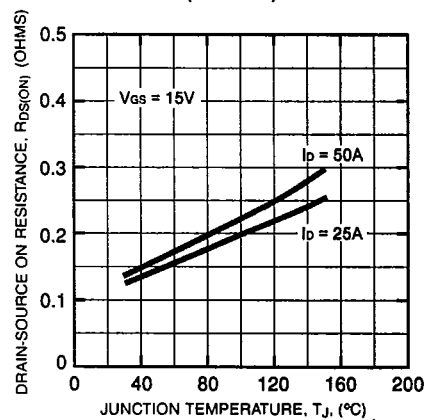
COMMON SOURCE OUTPUT CHARACTERISTICS (TYPICAL)



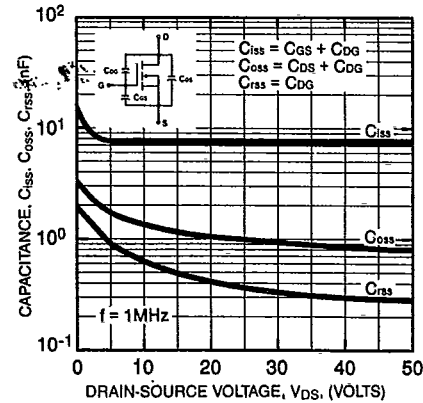
GATE THRESHOLD VOLTAGE VS. TEMPERATURE (TYPICAL)



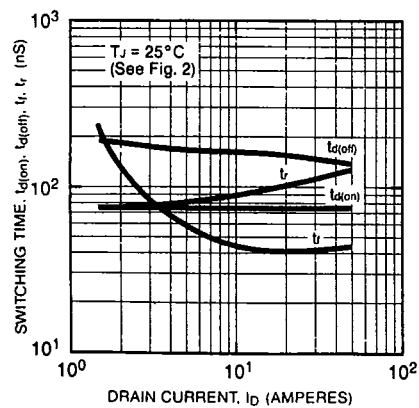
ON-RESISTANCE VS. TEMPERATURE (TYPICAL)



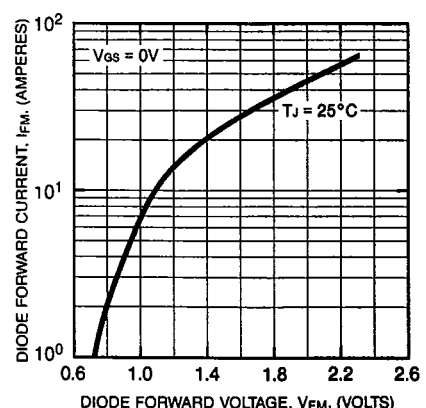
CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)



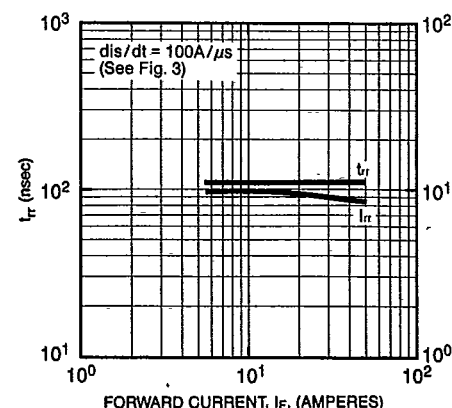
SWITCHING CHARACTERISTICS (TYPICAL)



DIODE CHARACTERISTICS (TYPICAL)



REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL)

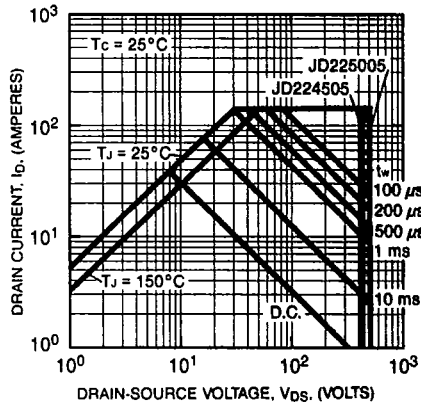




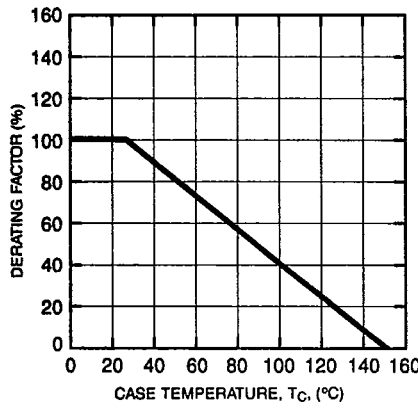
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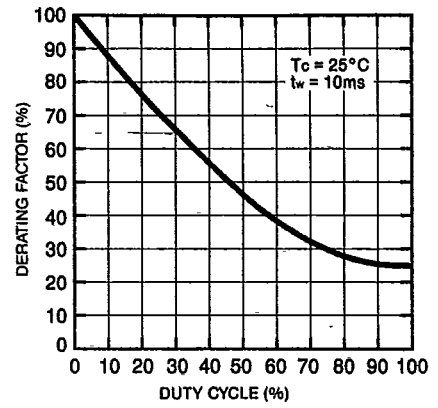
FORWARD BIAS SAFE OPERATING AREA (S.O.A.)



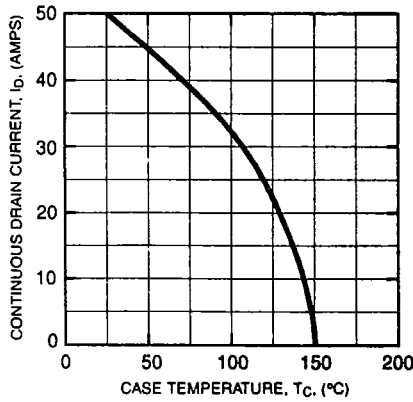
TEMPERATURE DERATING FACTOR OF SAFE OPERATING AREA (S.O.A.)



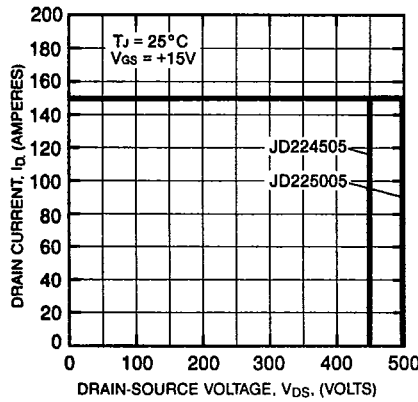
DUTY CYCLE DERATING FACTOR OF SAFE OPERATING AREA (S.O.A.)



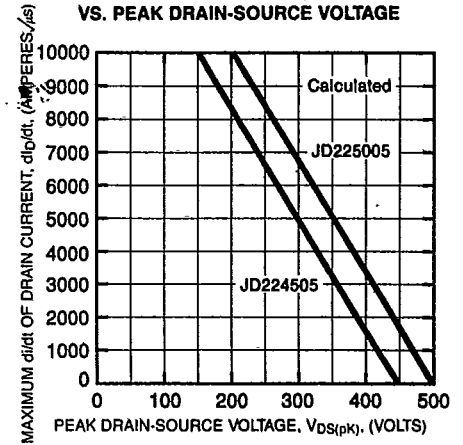
MAXIMUM DRAIN CURRENT VS. CASE TEMPERATURE



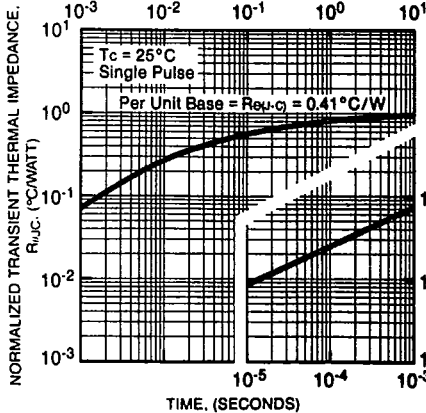
CLAMPED INDUCTIVE LOAD SWITCHING SAFE OPERATING AREA (S.S.O.A.)



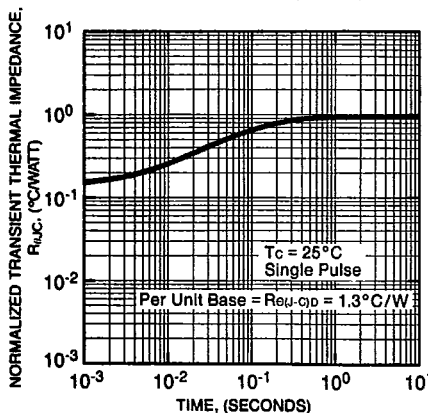
MAXIMUM TURN-OFF di/dt VS. PEAK DRAIN-SOURCE VOLTAGE



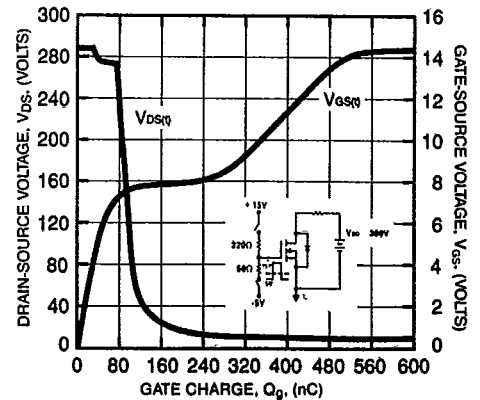
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MOSFET)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (DIODE)



GATE CHARGE VS. Vds AND Vgs (TYPICAL)





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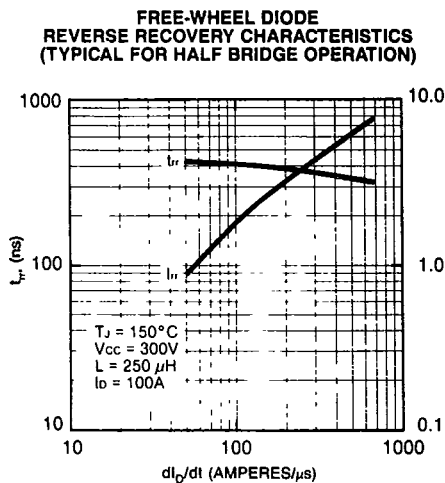
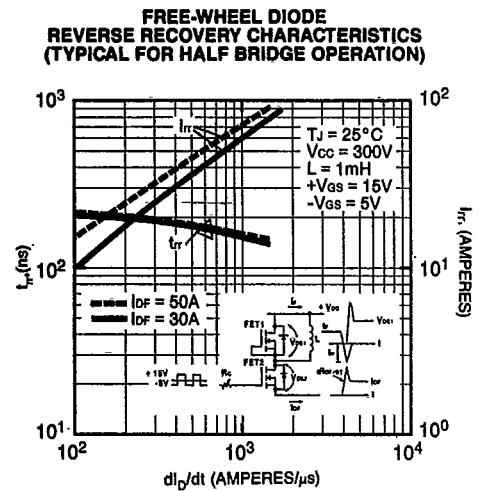
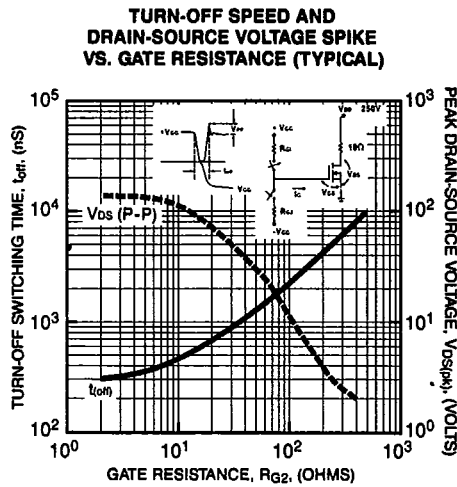
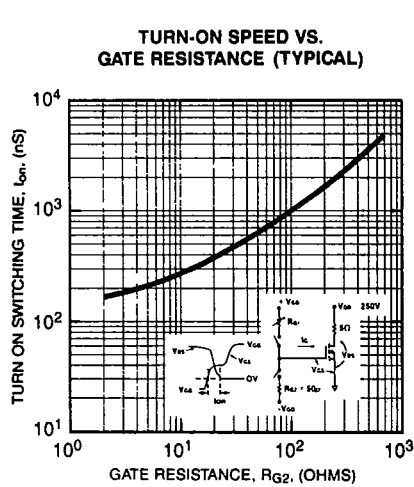


Figure 1 Switching Time Test Circuit 1

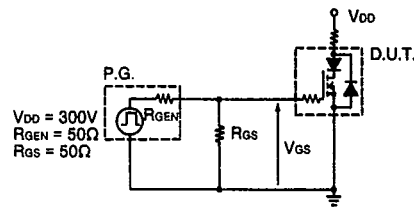


Figure 2 Switching Time Test Circuit 2

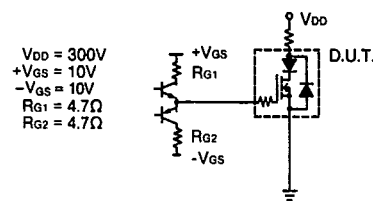


Figure 3 Reverse Recovery Test Circuit

