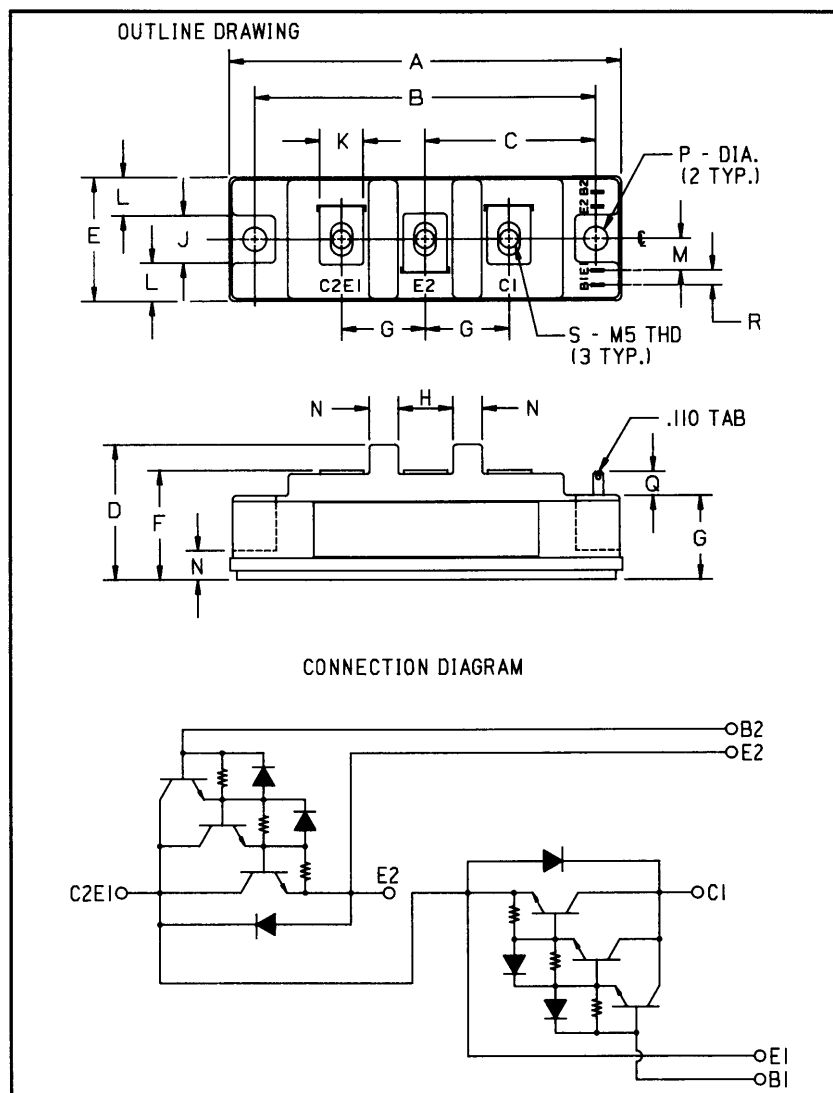


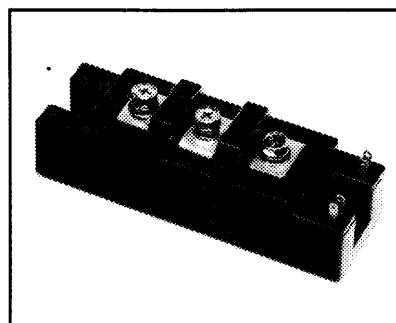
Dual Darlington Transistor Module 75 Amperes/1000 Volts



Outline Drawing

Dimensions	Inches	Millimeters
A	4.252 Max.	108 Max.
B	3.661 ± 0.012	93 ± 0.3
C	1.831	46.5
D	1.457	37
E	1.339 Max.	34 Max.
F	1.181 Max.	30 Max.
G	0.906	23
H	0.591	15
J	0.512	13

Dimensions	Inches	Millimeters
K	0.472	12
L	0.413	10.5
M	0.344	8.75
N	0.315	8
P	0.256 Dia.	6.5 Dia.
Q	0.256 Min.	6.5 Min.
R	0.157	4
S	M5 Metric	M5



Description:

The Powerex Dual Darlington Transistor Modules are high power devices designed for use in switching applications. The modules are isolated, consisting of two Darlington Transistors with each transistor having a reverse parallel connected high-speed diode.

Features:

- ☐ Isolated Mounting
- ☐ Planar Chips
- ☐ Discrete Fast Recovery Feedback Diode
- ☐ High Gain (h_{FE})
- ☐ Quick Connect Base-Emitter Signal Terminals
- ☐ Base-Emitter Speed-up Diodes

Applications:

- ☐ AC Motor Control
- ☐ DC Motor Control
- ☐ Switching Power Supplies
- ☐ Inverters

Ordering Information:

Example: Select the complete eight digit module part number you desire from the table - i.e. KD221K75 is a 1000 Volt, 75 Ampere Dual Darlington Module.

Type	$V_{CE0(sus)}$ Volts (1000)	Current Rating Amperes (75)
KD22	1K	75



Powerex, Inc., 200 Hillis Street, Youngwood, Pennsylvania 15697-1800 (412) 925-7272

KD221K75
Dual Darlington Transistor Module
75 Amperes/1000 Volts

Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Ratings	Symbol	KD221K75	Units
Junction Temperature	T_j	-40 to 150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 125	$^\circ\text{C}$
Collector-Emitter Sustaining Voltage, $V_{\text{BE}} = -2\text{V}$	$V_{\text{CEV(sus)}}$	1000	Volts
Collector-Base Voltage	V_{CBO}	1000	Volts
Emitter-Base Voltage	V_{EBO}	7	Volts
Collector-Emitter Voltage, $V_{\text{BE}} = -2\text{V}$	V_{CEV}	1000	Volts
Continuous Collector Current	I_{C}	75	Amperes
Diode Forward Current	I_{FM}	75	Amperes
Continuous Base Current	I_{B}	4	Amperes
Diode Surge Current	I_{FSM}	700	Amperes
Power Dissipation (Each Transistor)	P_t	500	Watts
Max. Mounting Torque M5 Terminal Screws	—	17	in.-lb.
Max. Mounting Torque M6 Mounting Screws	—	26	in.-lb.
Module Weight (Typical)	—	250	Grams
V Isolation	V_{RMS}	2500	Volts

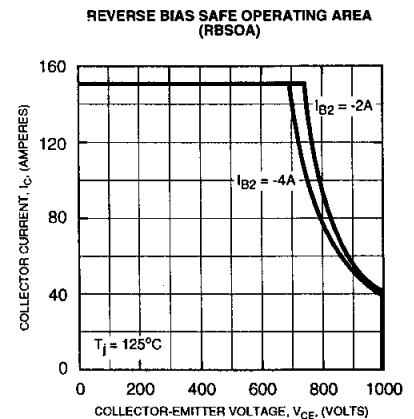
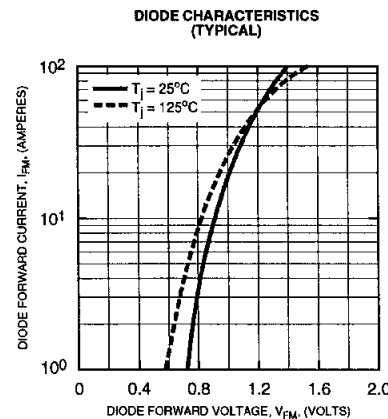
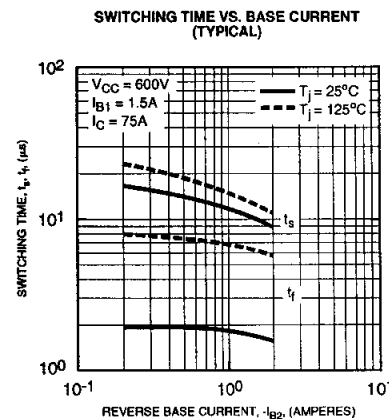
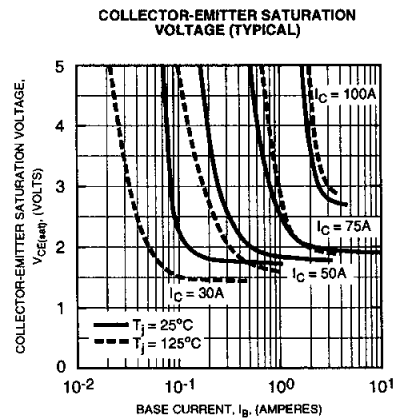
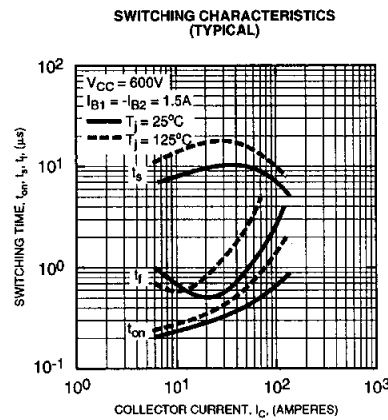
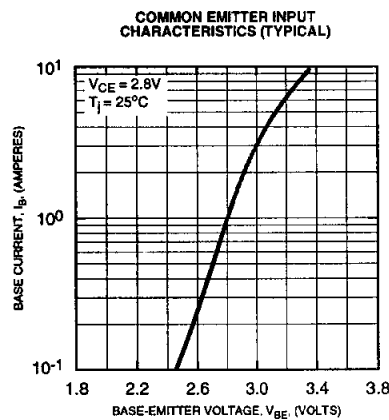
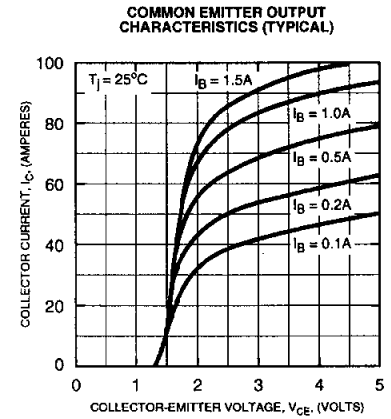
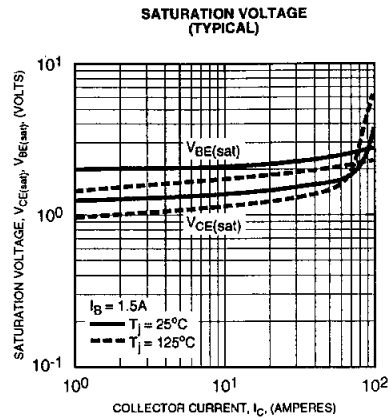
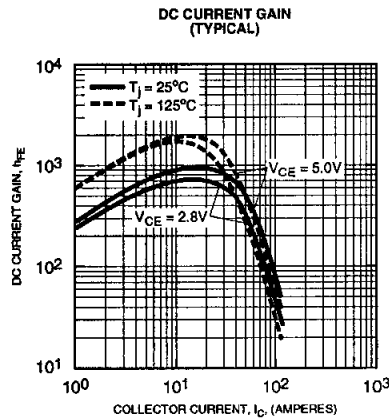
Electrical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics		Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector Cutoff Current		I_{CEV}	$V_{\text{CE}} = 1000\text{V}, V_{\text{BE}} = -2\text{V}$	—	—	1	mA
			$V_{\text{CE}} = 1000\text{V}, V_{\text{BE}} = -2\text{V}, T_{\text{C}} = 125^{\circ}\text{C}$	—	—	10	mA
Emitter Cutoff Current		I_{EBO}	$V_{\text{EB}} = 7\text{V}$	—	—	200	mA
DC Current Gain		h_{FE}	$I_{\text{C}} = 75\text{A}, V_{\text{CE}} = 5\text{V}$	100	—	—	—
Diode Forward Voltage		V_{FM}	$I_{\text{FM}} = 75\text{A}$	—	—	1.8	Volts
Collector-Emitter Saturation Voltage		$V_{\text{CE(sat)}}$	$I_{\text{C}} = 75\text{A}, I_{\text{B}} = 1.5\text{A}$	—	—	2.5	Volts
Base-Emitter Saturation Voltage		$V_{\text{BE(sat)}}$	$I_{\text{C}} = 75\text{A}, I_{\text{B}} = 1.5\text{A}$	—	—	3.5	Volts
Resistive Load	Turn-on	t_{on}	$V_{\text{CC}} = 600\text{V}$	—	—	2.5	μs
	Storage Time	t_{s}	$I_{\text{C}} = 75\text{A}$	—	—	15	μs
Switch Times	Fall Time	t_{f}	$I_{\text{B1}} = 1.5\text{A}, I_{\text{B2}} = -1.5\text{A}$	—	—	3.0	μs

Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Case-to-Sink	$R_{\theta(\text{c-s})}$	Per 1/2 Module	—	—	0.15	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta(\text{j-c})}$	Transistor Part	—	—	0.25	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta(\text{j-c})}$	Diode Part	—	—	1.2	$^\circ\text{C/W}$

KD221K75
Dual Darlington Transistor Module
75 Amperes/1000 Volts



KD221K75

Dual Darlington Transistor Module

75 Amperes/1000 Volts

