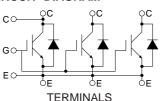
TENTATIVE SPECIFICATION

N1200E33[

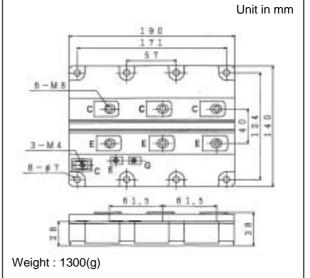
Silicon N-channel IGBT

FEATURES

- * High speed, low loss IGBT module.
- * Low driving power due to low input
- capacitance MOS gate.
- * Low noise due to ultra soft fast recovery diode.
- * High reliability, high durability module.
- * High thermal fatigue durability. (delta Tc=70°C, N>30,000cycles)
- * Isolated head sink (terminal to base). **CIRCUIT DIAGRAM**



OUTLINE DRAWING



ABSOLUTE MAXIMUM RATINGS (Tc=25°C)

Item		Symbol	Unit	MBN1200E33D
Collector Emitter Voltage		V _{CES}	V	3,300
Gate Emitter Voltage		V _{GES}	V	±20
Collector Current	DC	I _C	А	1,200
	1ms	I _{Cp}		2,400
Forward Current	DC	I _F	А	1,200
Forward Current	1ms	I _{FM}		2,400
Junction Temperature		T _i	°C	-40 ~ +125
Storage Temperature		T _{stq}	°C	-40 ~ +125
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Screw Torque	erminals (M4/M8)	-	N·m	2/10 (1)
	Nounting (M6)	-		6 (2)

Notes: (1) Recommended Value 1.8±0.2/9±1N·m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Symbol Item Unit Min Тур Max. **Test Conditions** V_{CE}=3,300V, V_{GE}=0V, Tj=25°C 12 Collector Emitter Cut-Off Current I_{CES} mΑ V_{CE}=3,300V, V_{GE}=0V, Tj=125°C 20 60 Gate Emitter Leakage Current I_{GES} -500 V_{GE}=±20V, V_{CE}=0V, Tj=25°C nA +500 Collector Emitter Saturation Voltage I_C=1,200A, V_{GE}=15V, Tj=125°C V 4.2 5.2 V_{CE(sat)} Gate Emitter Threshold Voltage V_{GE(TO)} V 4.5 6.0 7.0 V_{CE}=10V, I_C=1,200mA, Tj=25°C V_{CE}=10V, V_{GE}=0V, f=100kHz, Tj=25°C Input Capacitance Cies nF 110 _ **Internal Gate Resistance** Rge 1.2 V_{CE}=10V, V_{GE}=0V, f=100kHz, Tj=25°C Ω -**Rise Time** 1.9 3.1 V_{CC}=1,650V, Ic=1,200A tr -Turn On Time 2.4 3.3 L=100nH ton -Switching Times μS Fall Time 1.0 2.5 $R_G=3.3\Omega$ (3) tf -Turn Off Time V_{GE}=±15V, Tj=125°C toff 3.0 5.1 -Ic=1,200A, V_{GE}=0V, Tj=125°C Peak Forward Voltage Drop V_{FM} V 2.5 3.0 -Vcc=1,650V, Ic=1,200A, L=100nH **Reverse Recovery Time** 0.6 1.1 t_{rr} μS -Tj=125°C Turn On Loss V_{CC}=1,650V, Ic=1,200A, L=100nH J/P 2.1 E_{on(10%)} 1.6 _ Turn Off Loss J/P 1.3 1.7 $R_G=3.3\Omega$ _ (3) E_{off(10%)} V_{GE}=±15V, Tj=125°C J/P **Reverse Recovery Loss** E_{rr(10%)} 1.2 1.9 _ Stray inductance module 12 LSCE nΗ -IGBT 0.0085 Rth(j-c) --°C/W Junction to case Thermal Impedance FWD Rth(j-c) -0.017 -Contact °C/W Thermal Impedance Rth(c-f) 0.006 Case to fin -

Notes:(3) R_G value is the test condition's value for evaluation of the switching times, not recommended value.

Please, determine the suitable R_G value after the measurement of switching waveforms (overshoot voltage, etc.) with appliance mounted.

* Please contact our representatives at order.

* For improvement, specifications are subject to change without notice.

* For actual application, please confirm this spec sheet is the newest revision.



HITACHI POWER SEMICONDUCTORS

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